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February 23, 2004

MAIL STOP PATENT APPLICATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Re:

Application of Bernard MURPHY, Pratyush Kumar PRASOON and Manish

BHATIA

PATTERN RECOGNITION IN AN INTEGRATED CIRCUIT DESIGN

Assignee: ATRENTA, INC.

Our Ref. Q74354

Dear Sir:

Attached hereto is the application identified above comprising thirty-four (34) sheets of the specification, including the claims and abstract and seven (7) sheets of drawings. The requisite U.S. Government Filing Fee, copies of the executed Declaration and Power of Attorney and Assignment will be submitted at a later date.

Applicant claims benefit of small entity status in accordance with 37 CFR § 1.27.

The Government filing fee is calculated as follows (Small Entity fees apply):

Total claims	76 - 20	= 56 x	\$9.00 =	\$504.00
Independent claims	3 - 3	= x	\$43.00 =	\$.00
Base Fee				\$385.00

TOTAL FEE \$889.00

Benefit is claimed from:

<u>Country</u> <u>Application No</u>

Filing Date

U.S. Provisional

60/452,970

March 10, 2003

Respectfully submitted, SUGHRUE MION, PLLC

Attorneys for Applicant

Darryl **Me**xic

Registration No. 23,063

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CUSTOMER NUMBER

DM/tmm